

FIG. 1

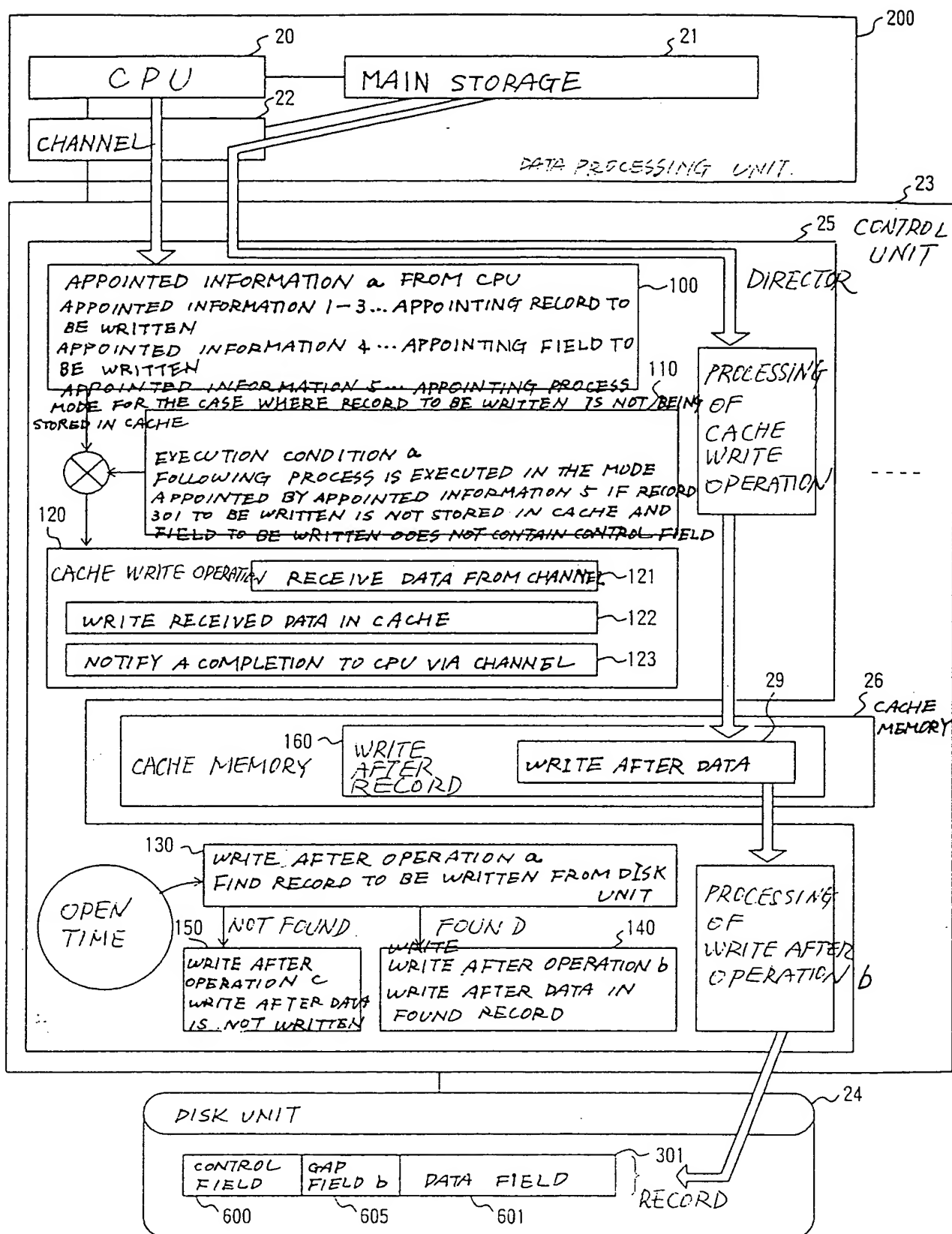


FIG. 2

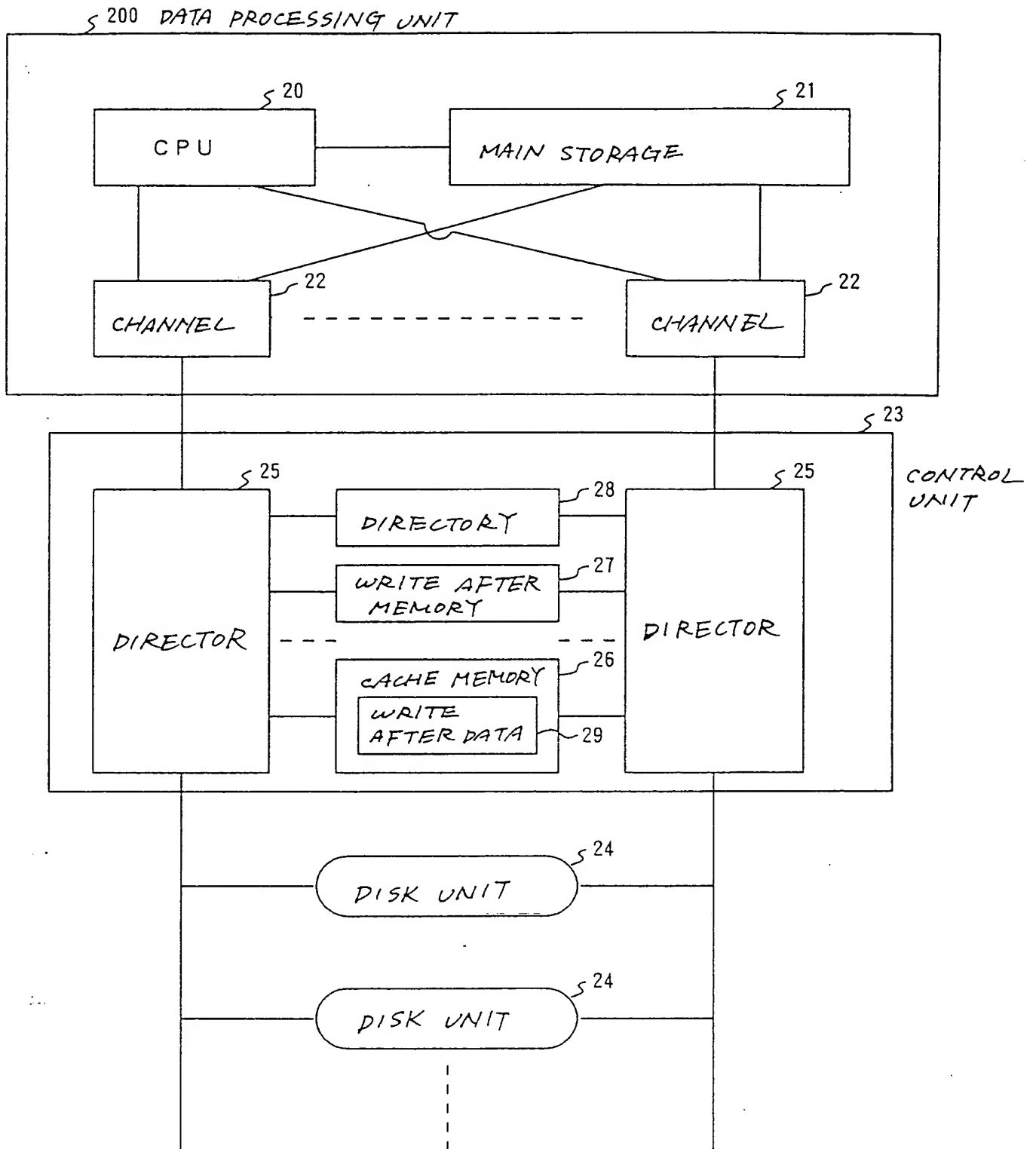


FIG. 3

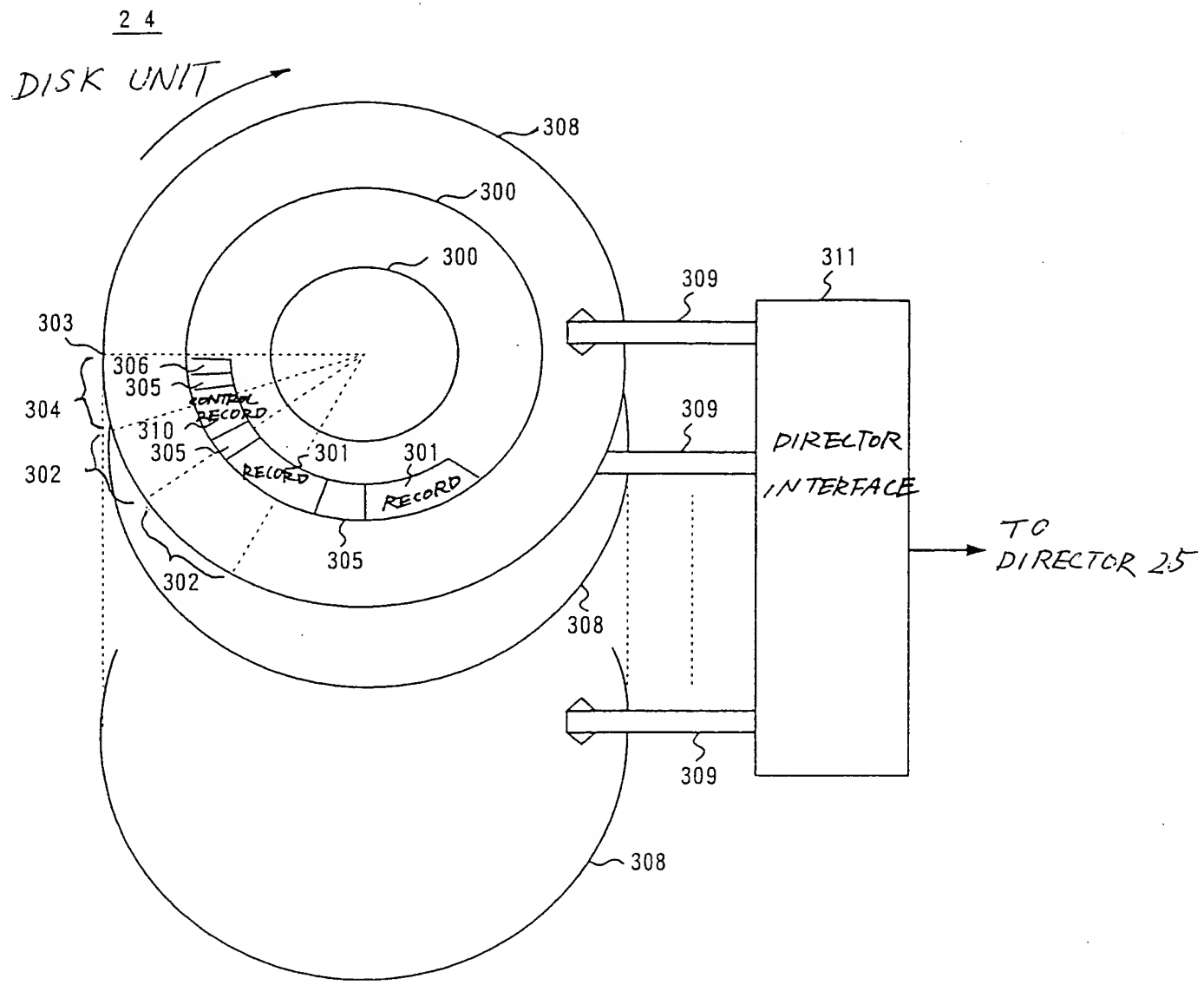


FIG. 4

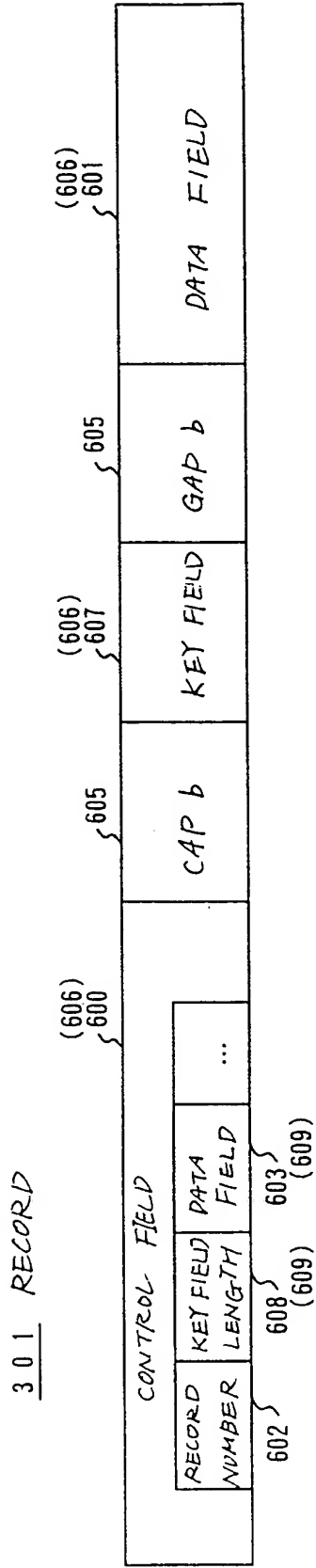


FIG. 5

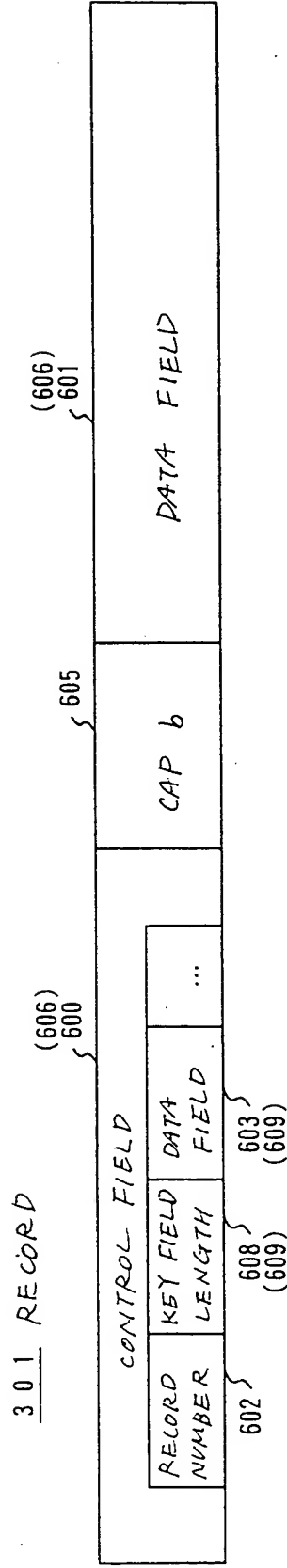


FIG. 6

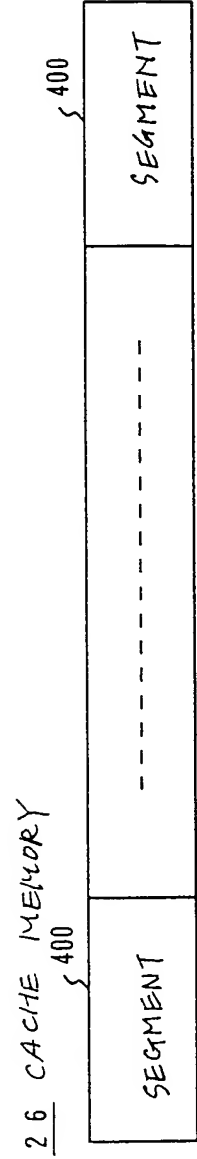
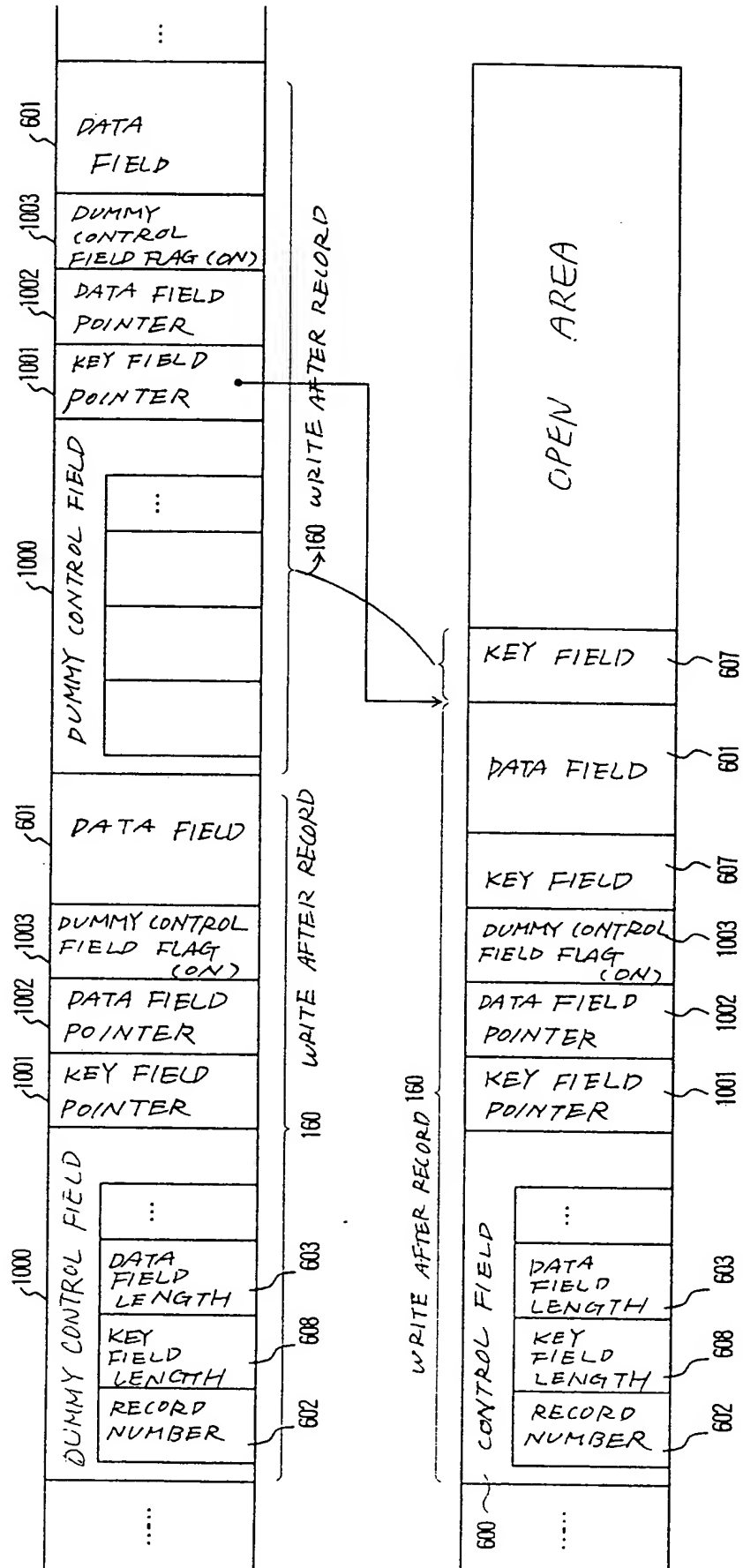


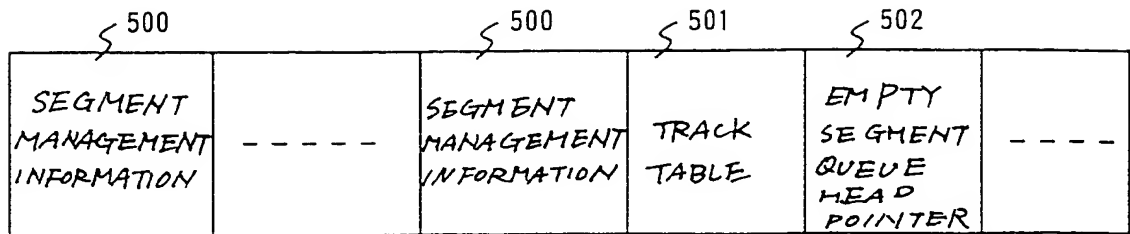
FIG. 7

SEGMENT  
400



# FIG. 8

28 DIRECTORY



# FIG. 9

SEGMENT MANAGEMENT INFORMATION

500

AS MANY AS THE  
NUMBER OF RECORD  
NUMBERS 602  
DEFINABLE WITHIN  
TRACK 300

AS MANY AS THE  
NUMBER OF RECORD  
NUMBERS 602  
DEFINABLE WITHIN  
TRACK 300

AS MANY AS THE  
NUMBER OF RECORD  
NUMBERS 602  
DEFINABLE WITHIN  
TRACK 300

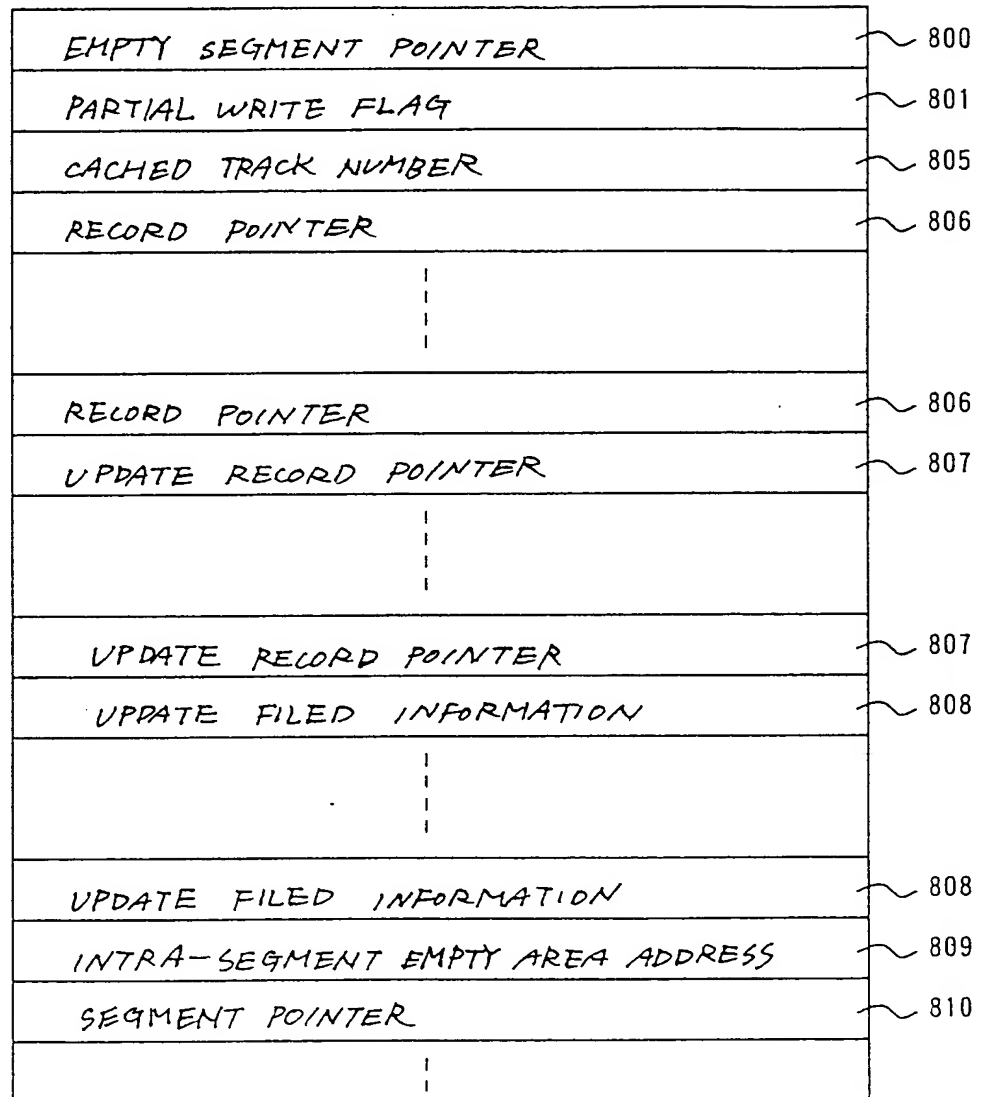


FIG. 10

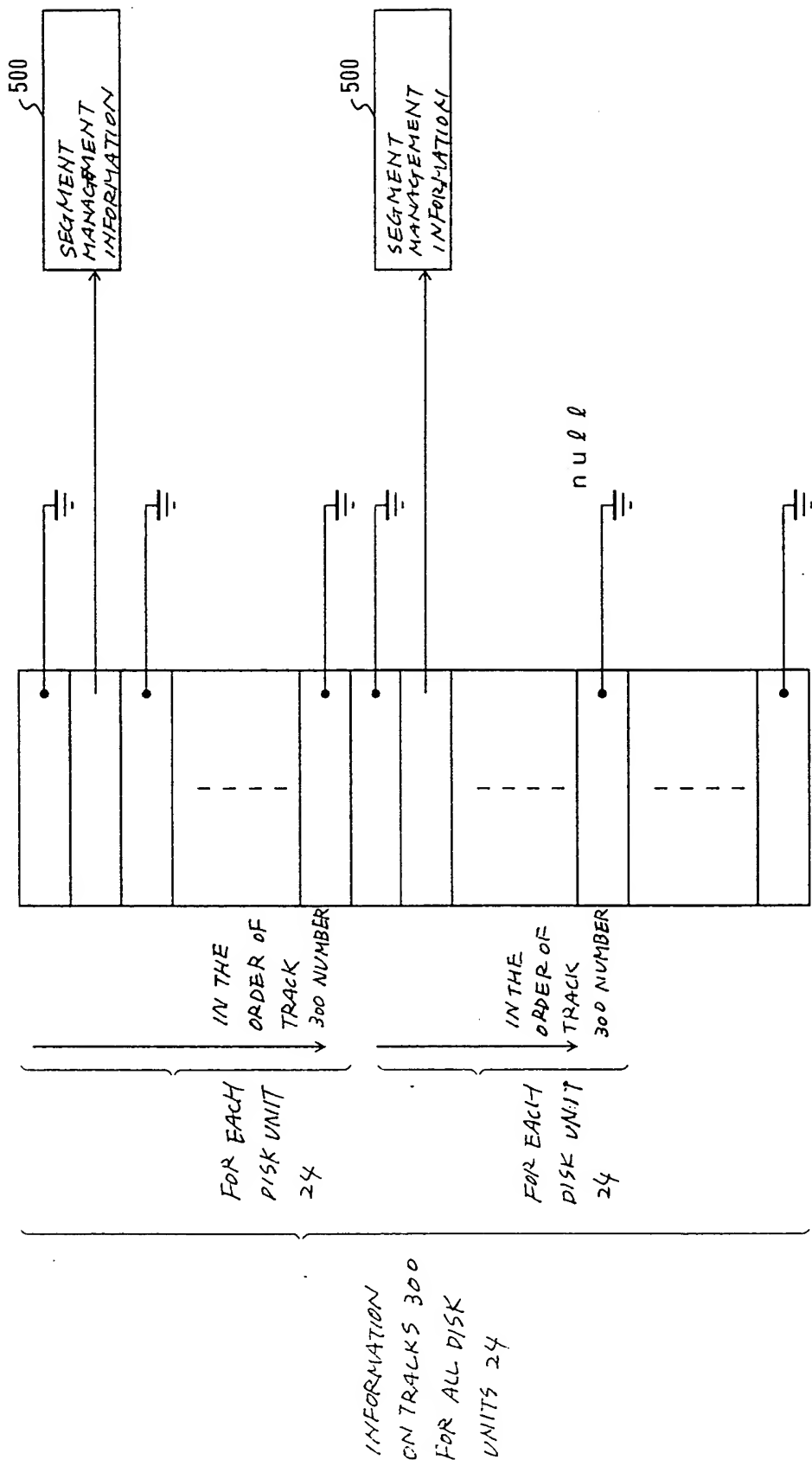


FIG. 11

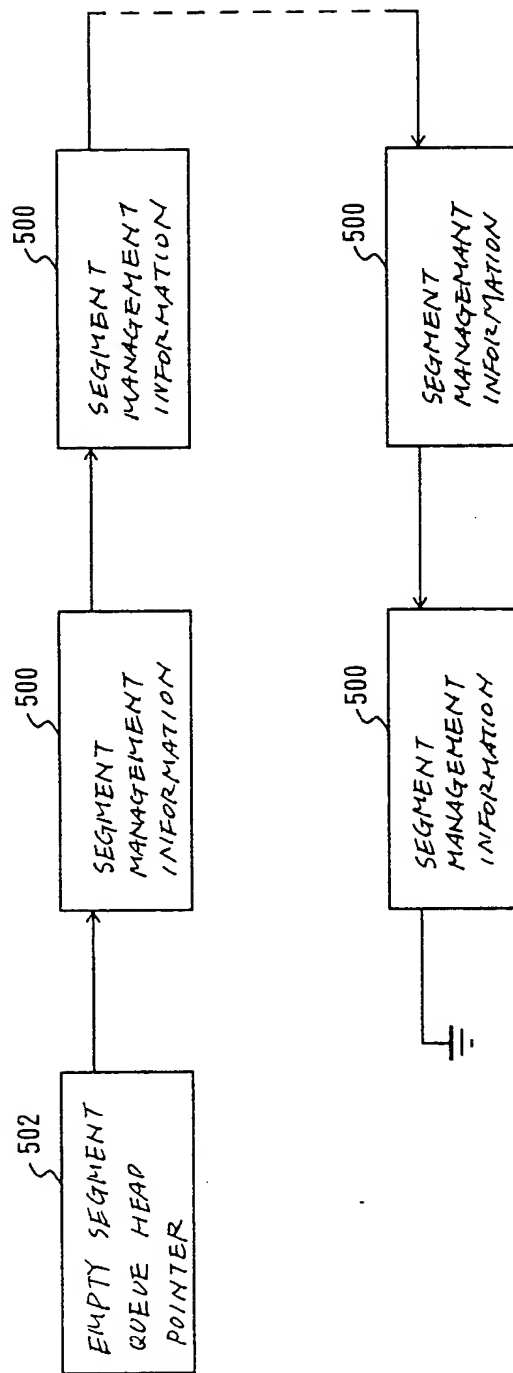




FIG. 12

27 WRITE AFTER MEMORY

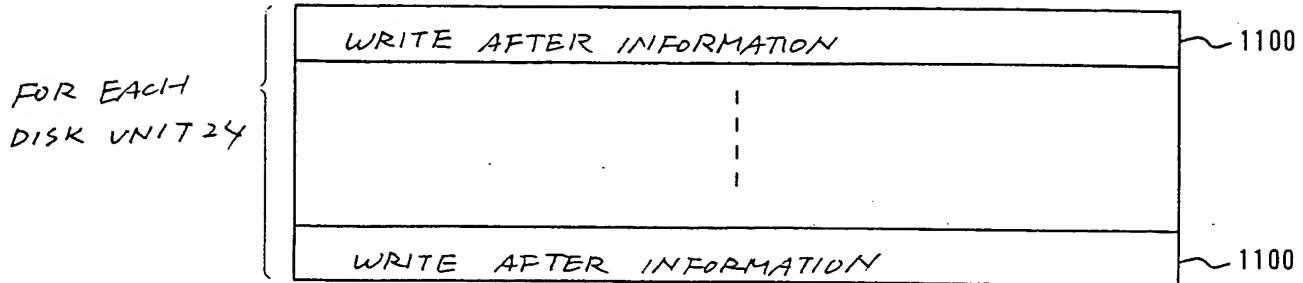


FIG. 13

1100 WRITE AFTER INFORMATION

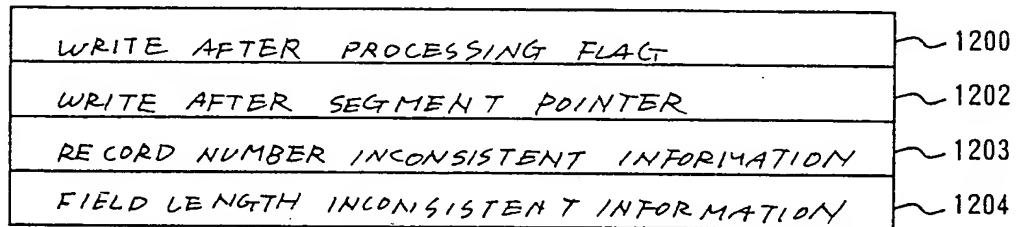


FIG. 14

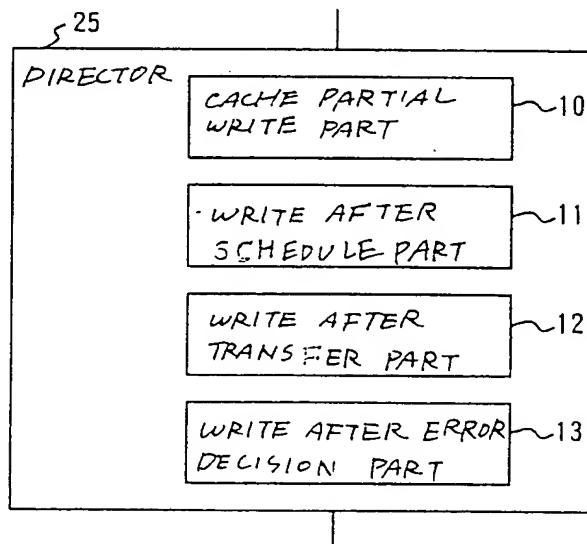


FIG. 15

10

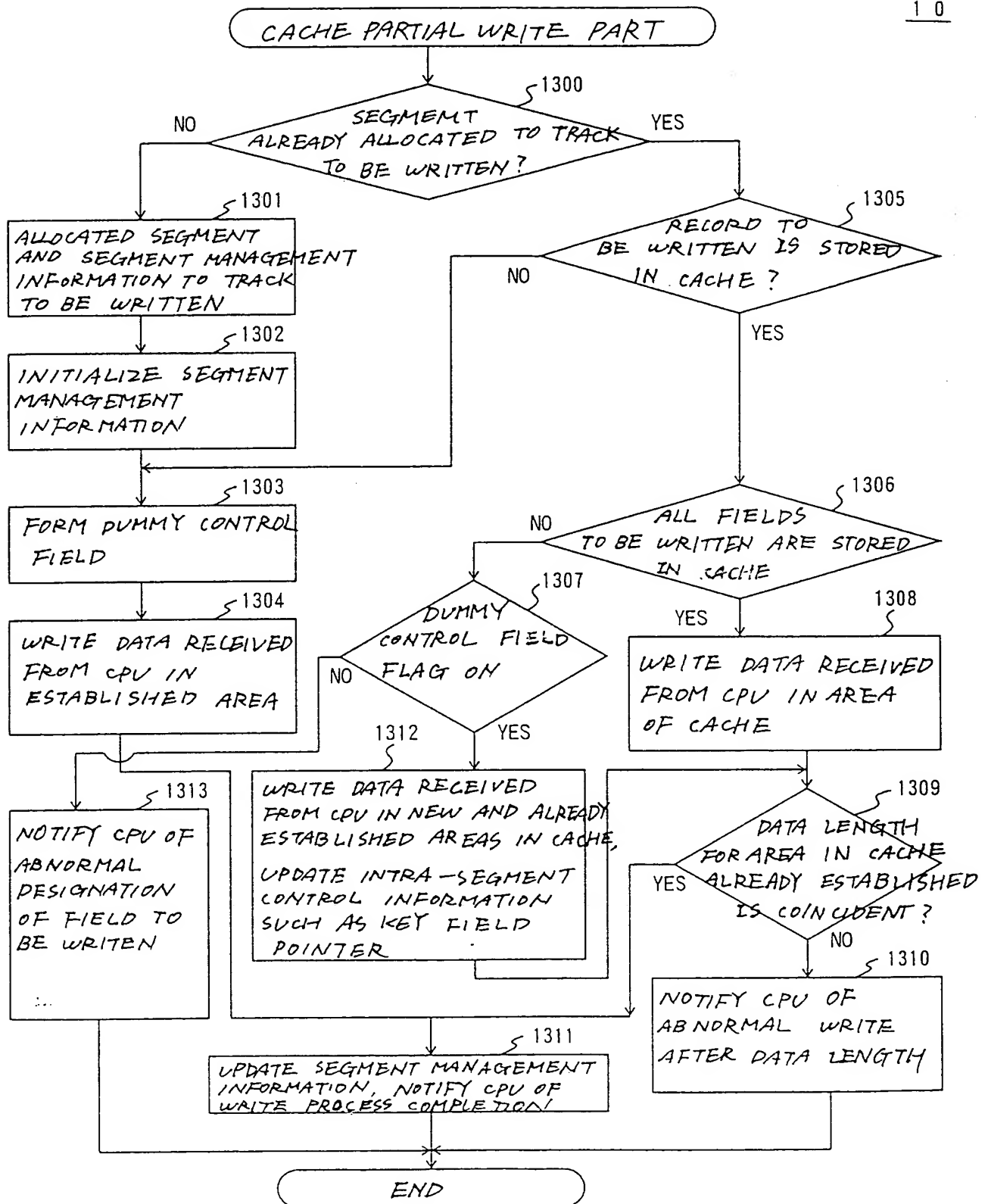


FIG. 16

11

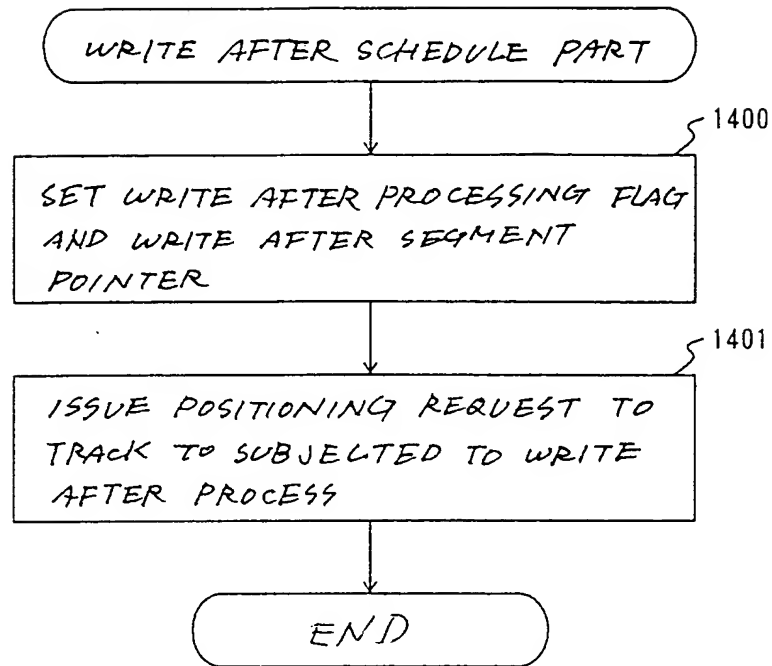


FIG. 17

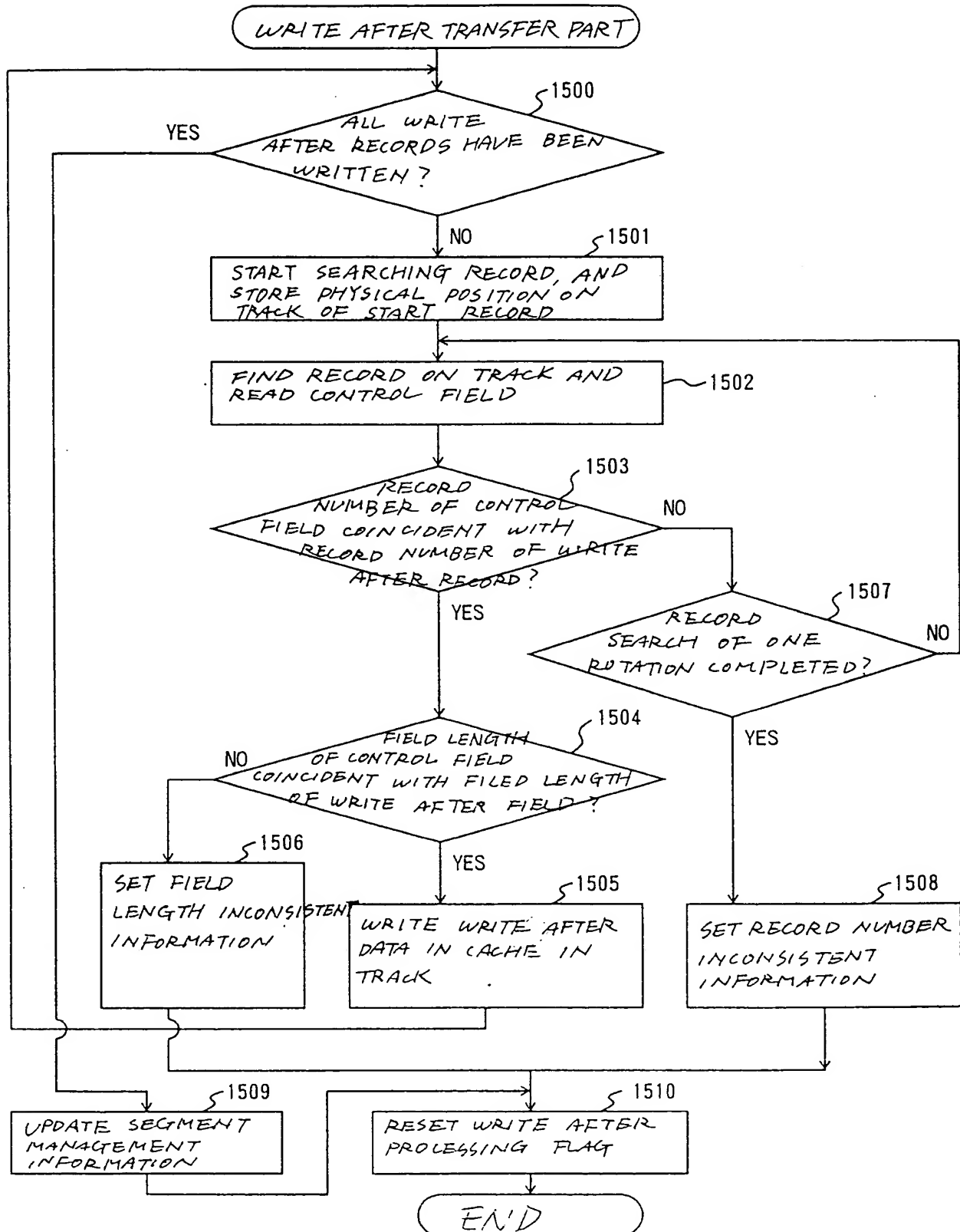


FIG. 18

13

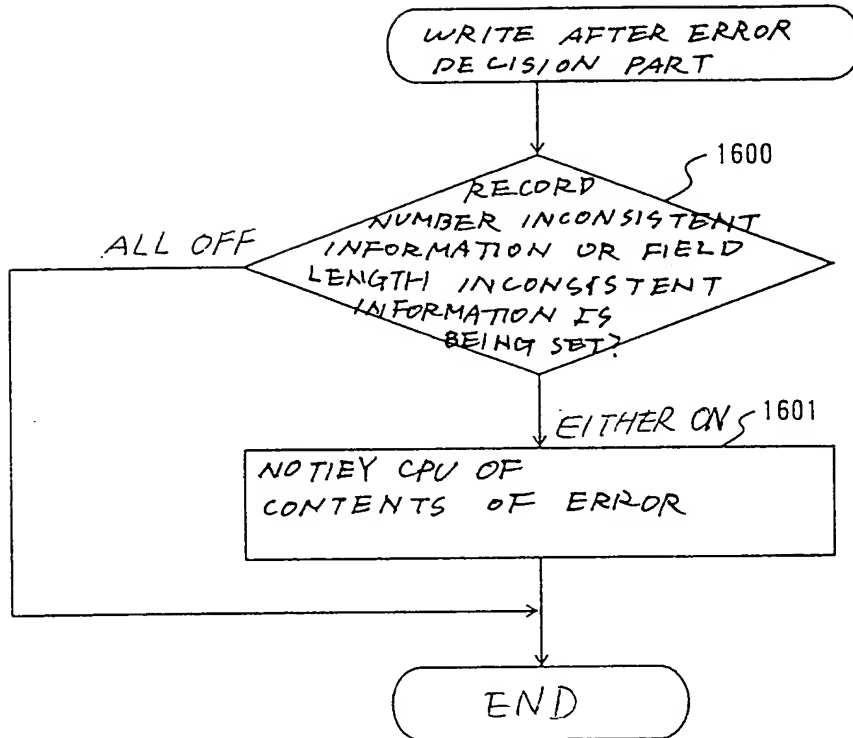


FIG. 19

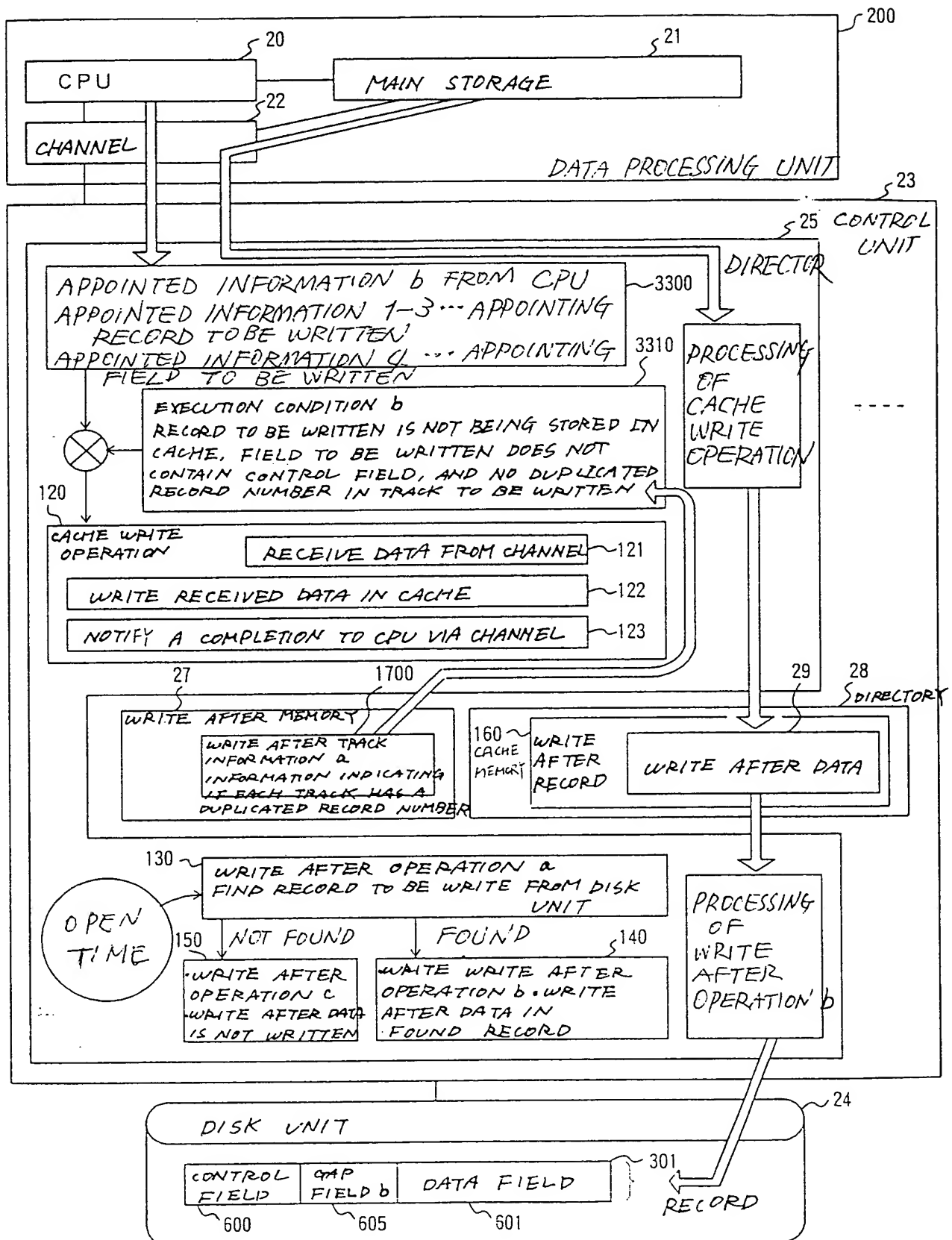


FIG. 20

2.7 WRITE AFTER MEMORY

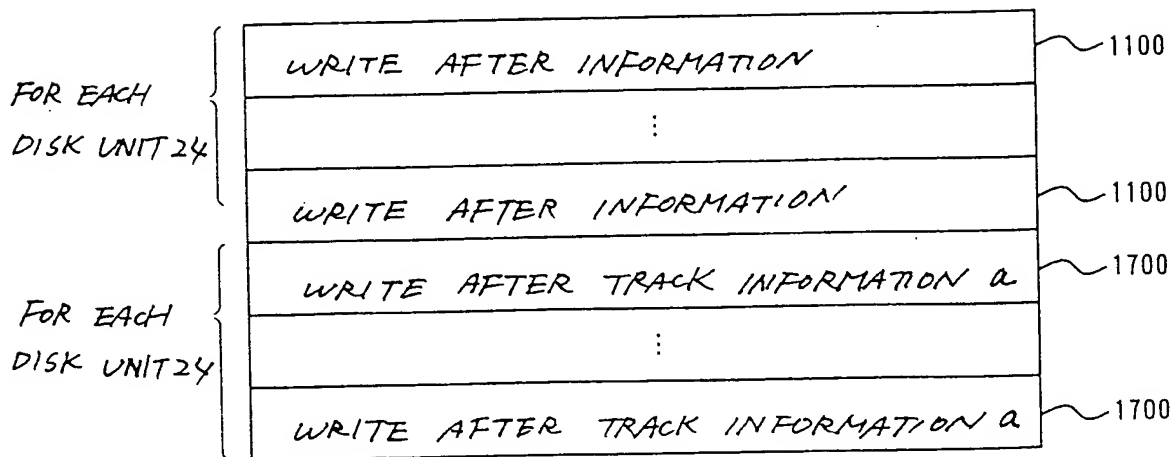


FIG. 21

1700 WRITE AFTER TRACK INFORMATION a

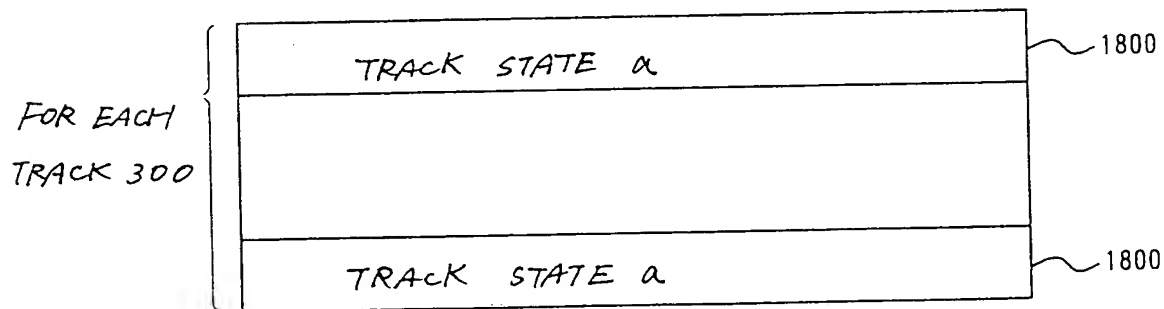


FIG. 22

1800

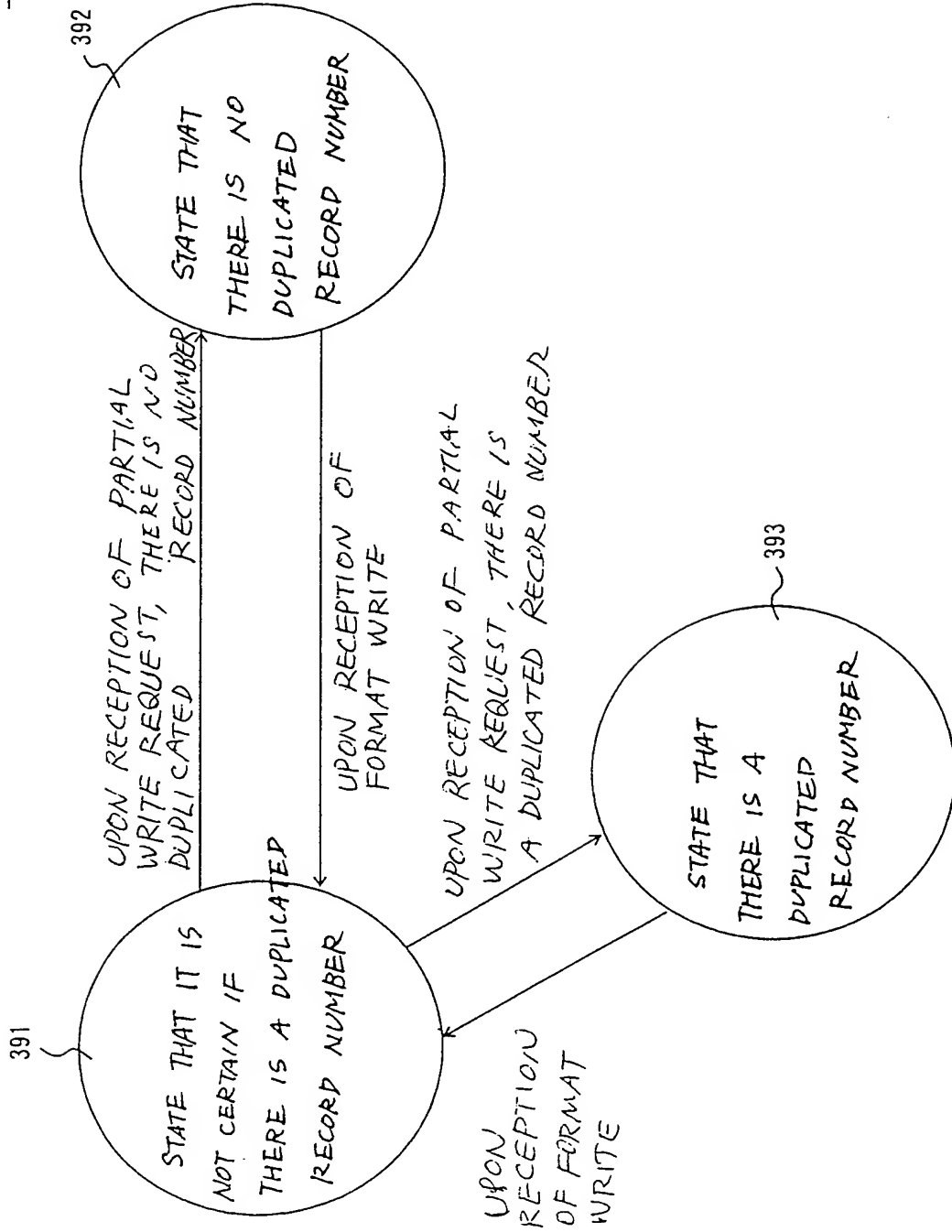




FIG. 23

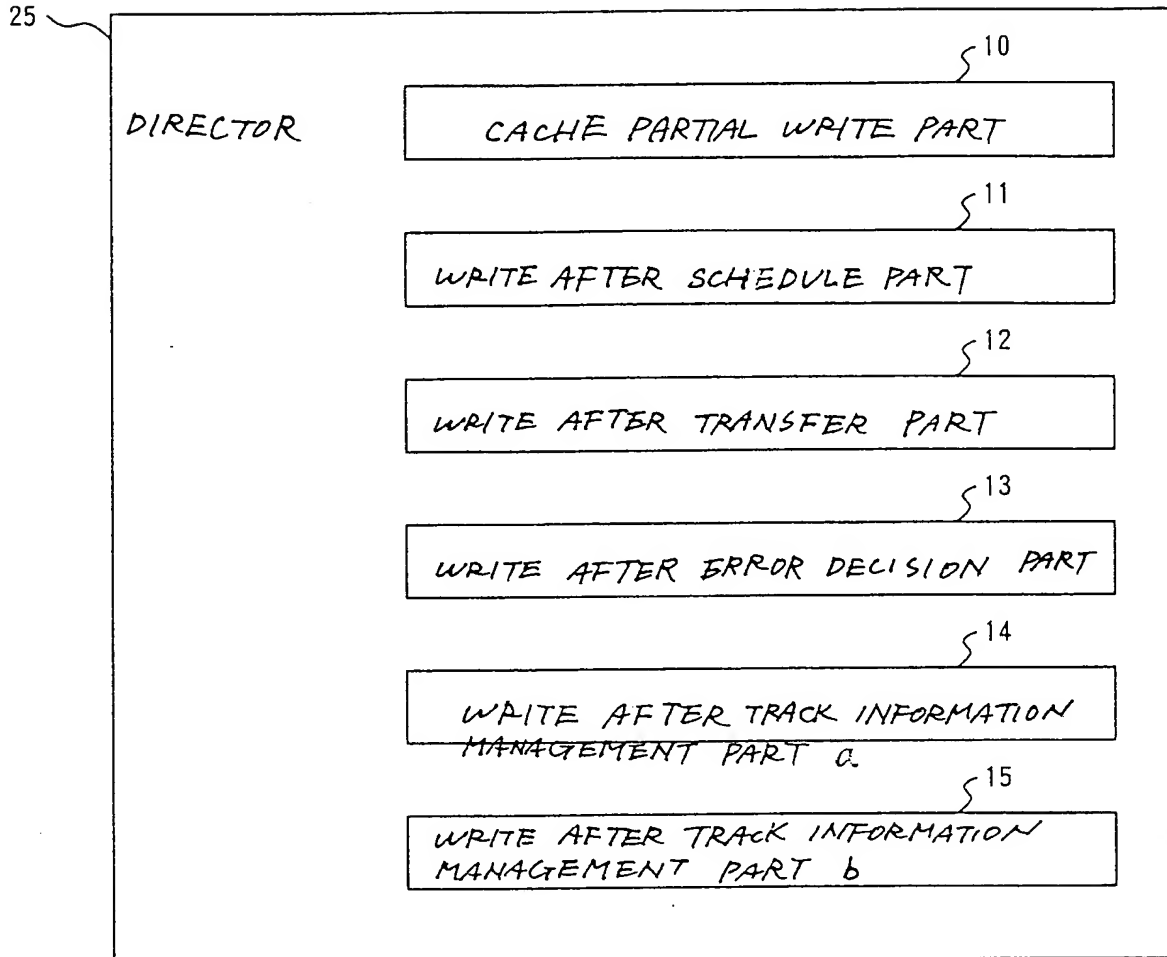


FIG. 24 A

14

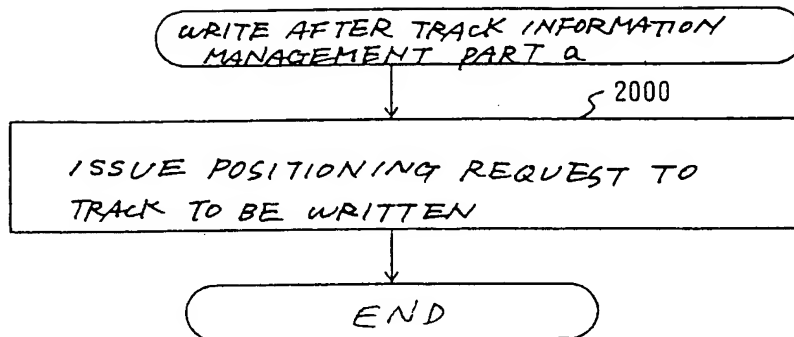


FIG. 24 B

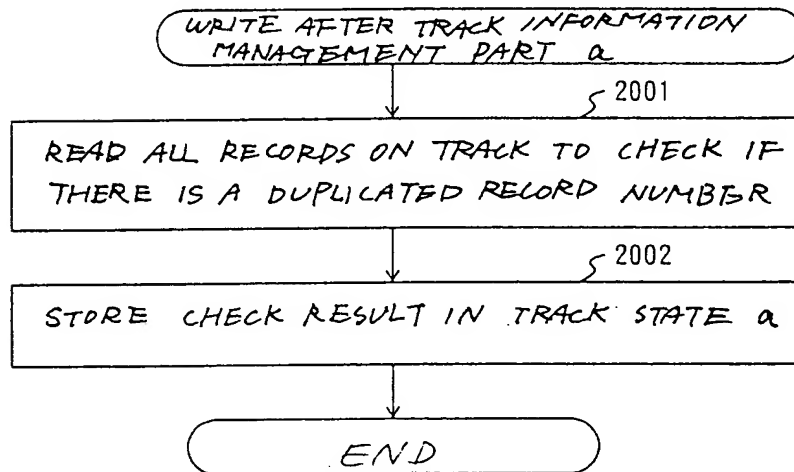


FIG. 25

15

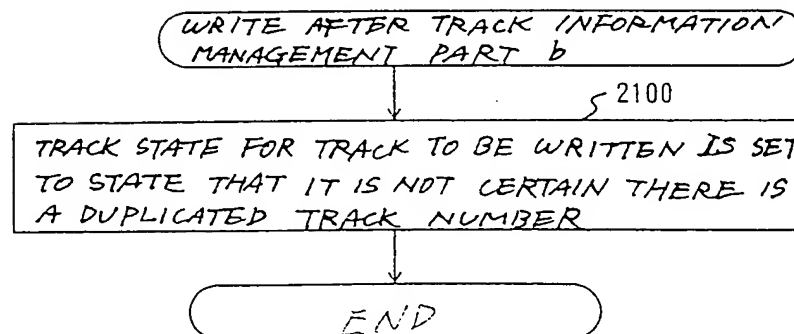
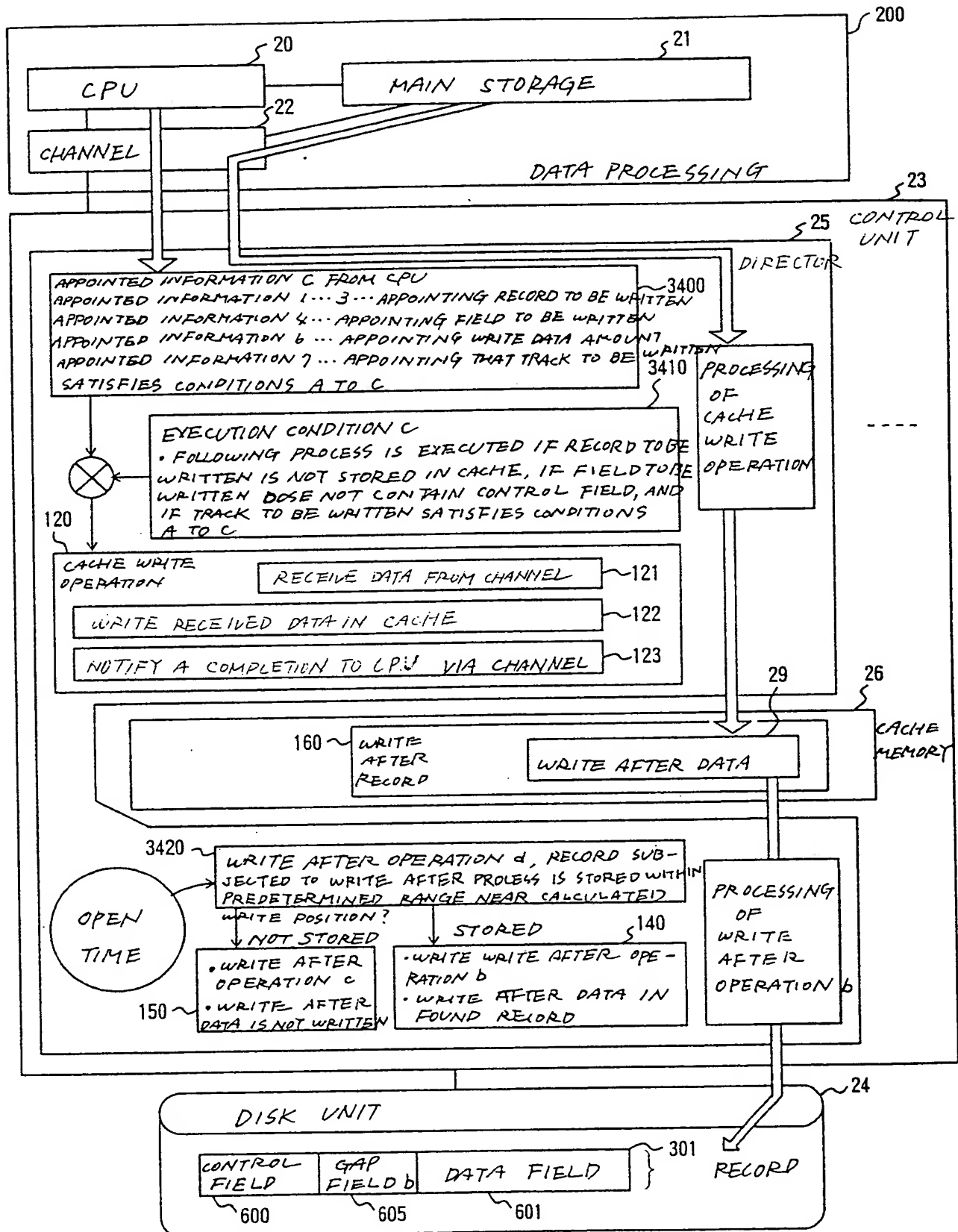


FIG. 26



# FIG. 27

5 0 0

## SEGMENT MANAGEMENT INFORMATION

AS MANY AS THE NUMBER  
OF RECORD NUMBERS  
602 DEFINABLE WITHIN  
TRACK 300

	EMPTY SEGMENT POINTER	800
	PARTIAL WRITE FLAG	801
	CACHE TRACK NUMBER	805
	RECORD POINTER	806
	:	
	RECORD POINTER	806
	UPDATE RECORD POINTER	807
	:	
	UPDATE RECORD POINTER	807
	UPDATE FIELD INFORMATION	808
	:	
	UPDATE FIELD INFORMATION	808
	INTRA-SEGMENT EMPTY AREA ADDRESS	809
	SEGMENT POINTER	810
	WRITE POSITION CALCULATION POSSIBLE BIT	2200
	FIXED DATA FIELD LENGTH	2201
	:	

# FIG. 28

1 1 0 0

## WRITE AFTER INFORMATION

WRITE AFTER PROCESSING FLAG	1200
WRITE AFTER SEGMENT POINTER	1202
RECORD NUMBER INCONSISTENT INFORMATION	1203
FIELD LENGTH INCONSISTENT INFORMATION	1204
WRITE AFTER START RECORD NUMBER	2300
RECORD PHYSICAL POSITION INCONSISTENT INFORMATION	2301

FIG. 29

10

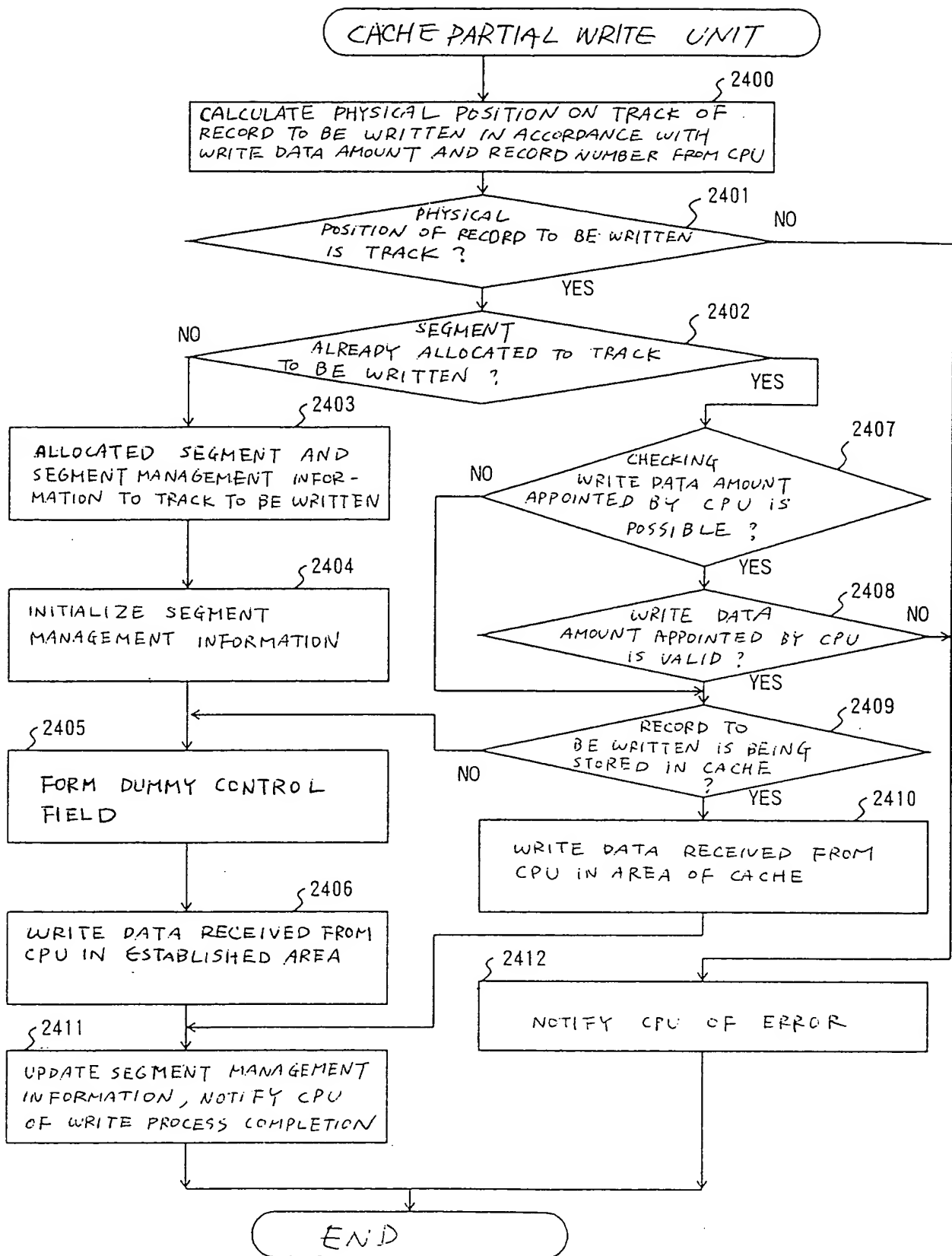


FIG. 30

11

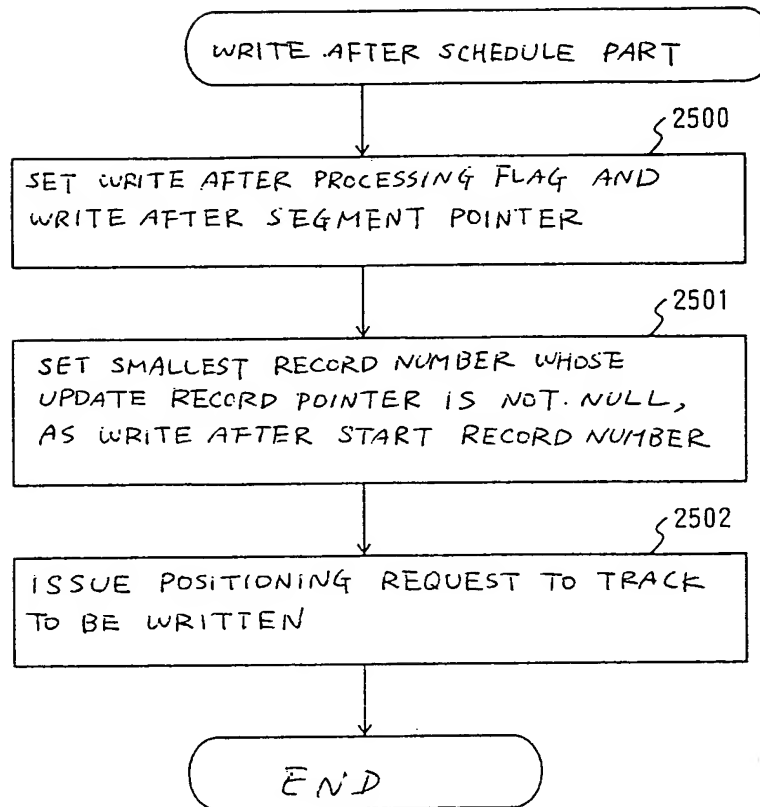


FIG. 31

12

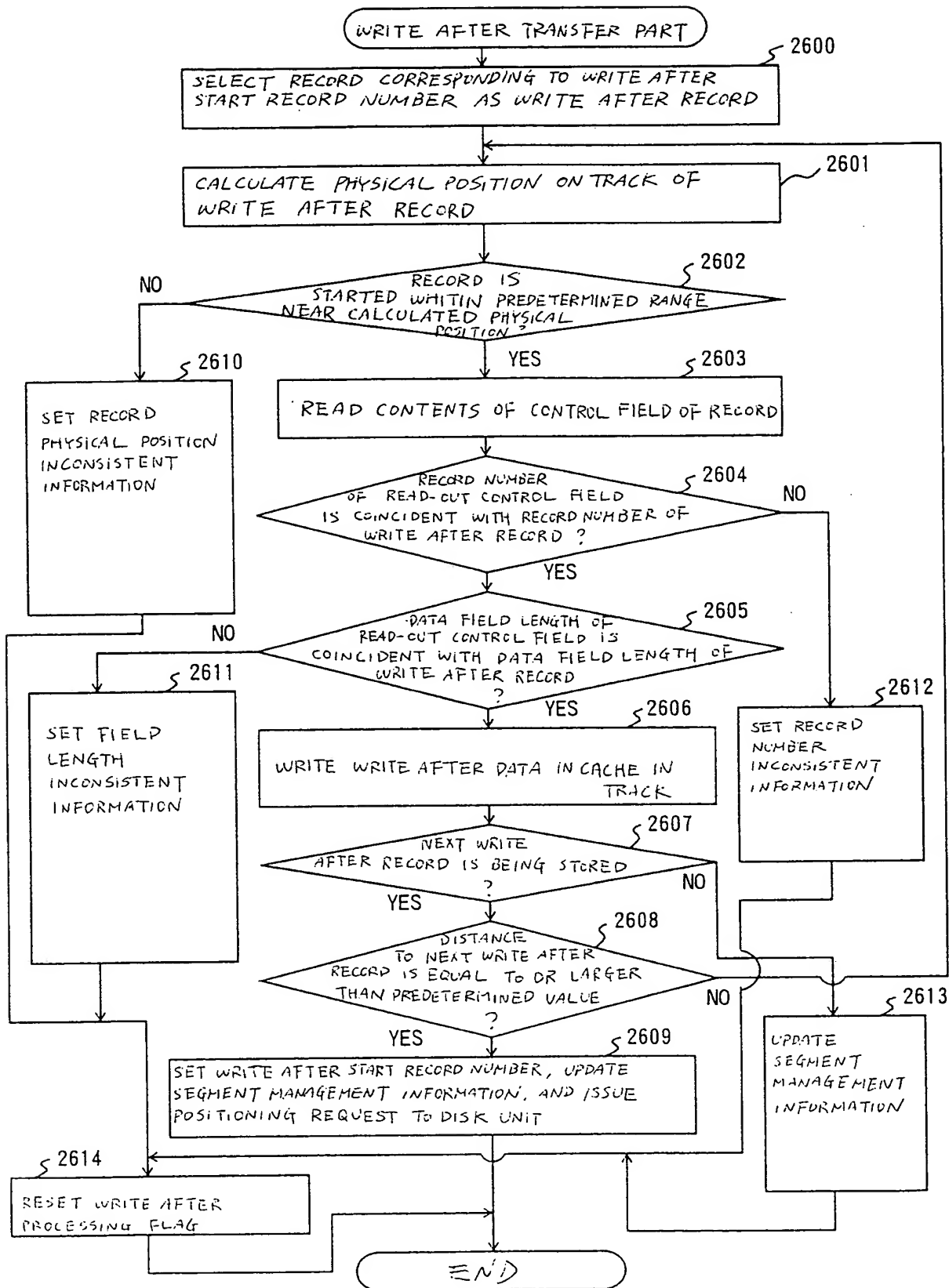


FIG. 32

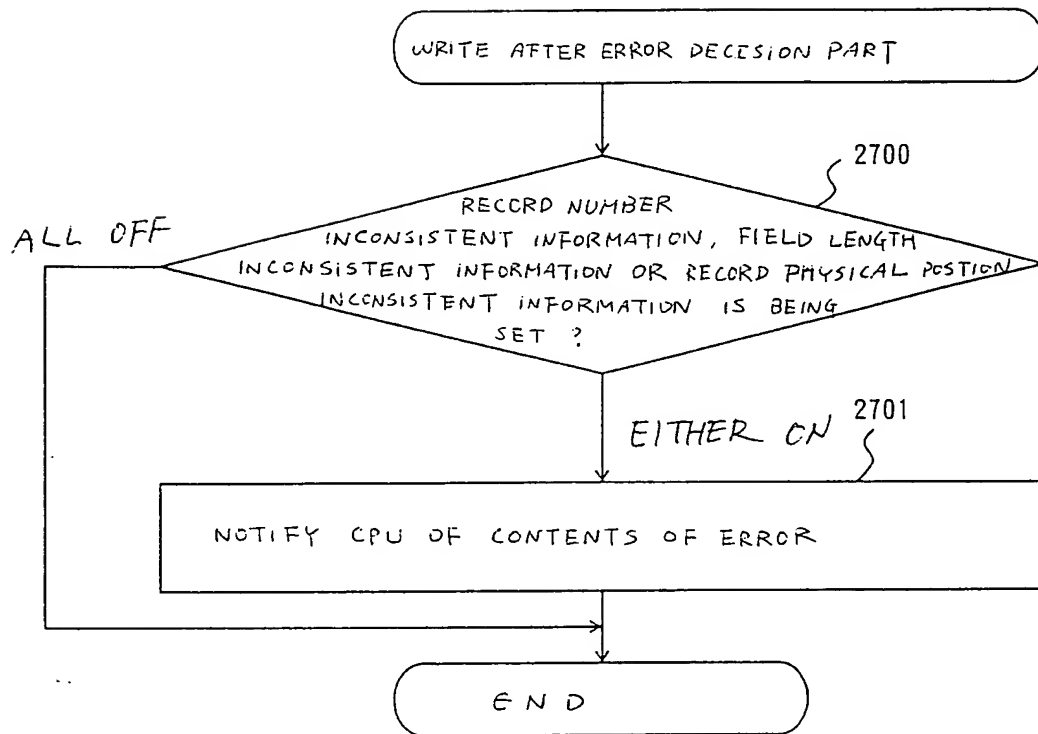




FIG. 33

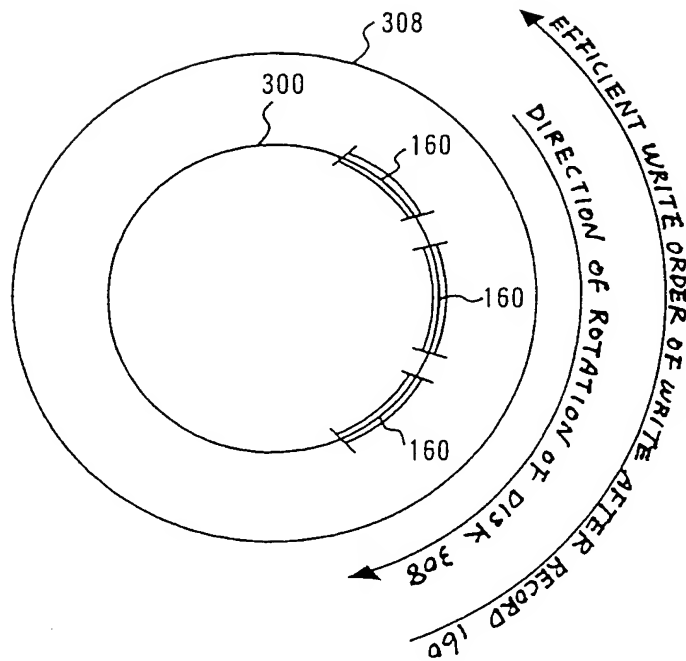


FIG. 34

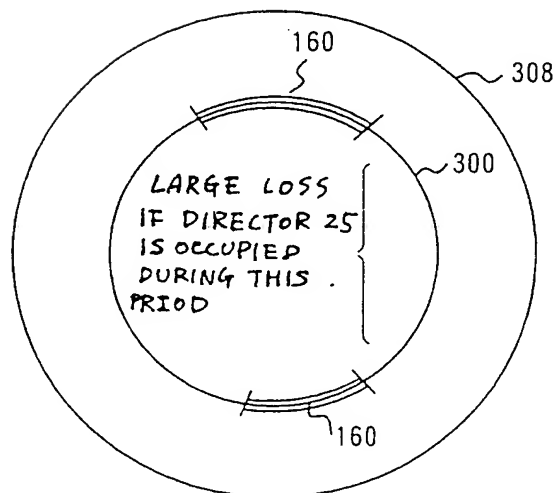
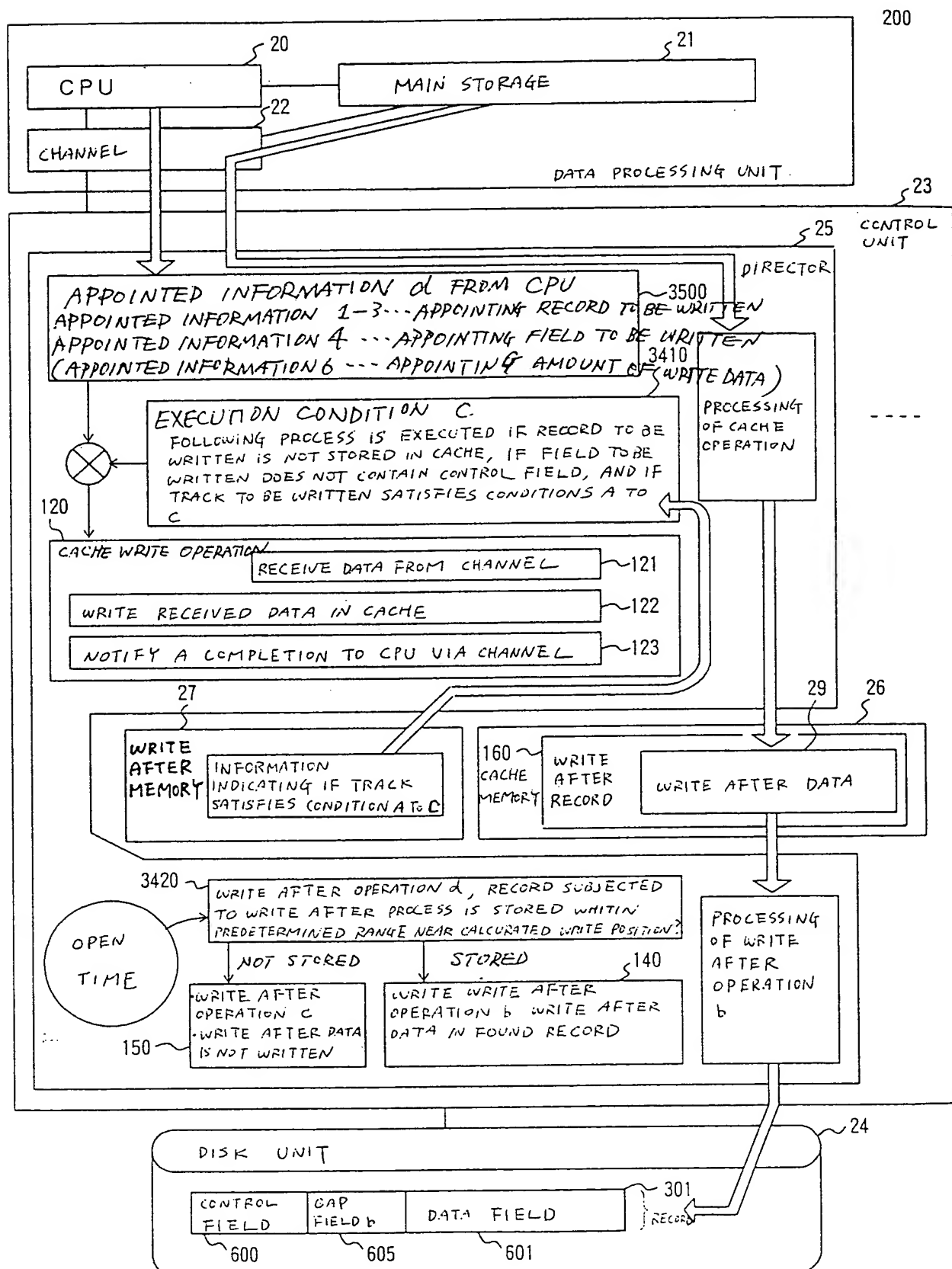


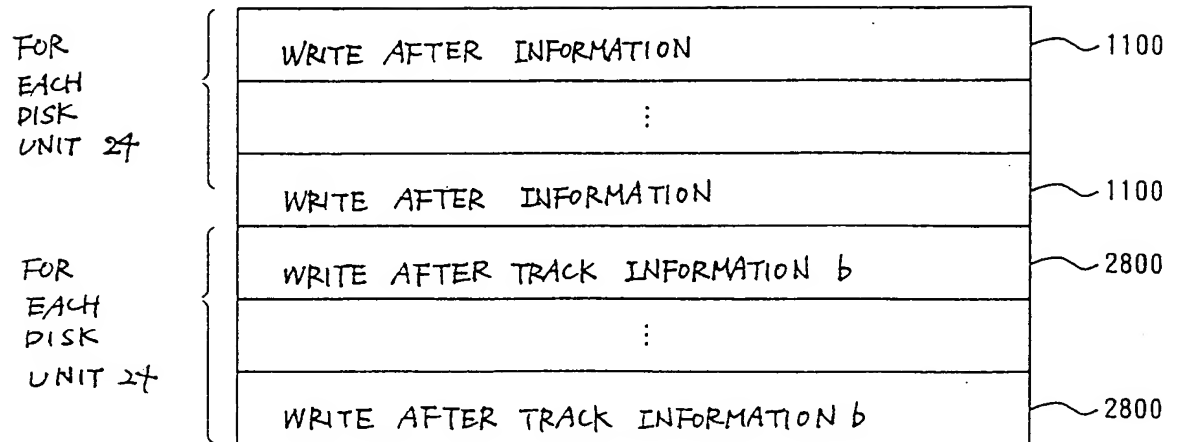
FIG. 35



# FIG. 36

WRITE AFTER MEMORY

27



# FIG. 37

WRITE AFTER TRACK INFORMATION

2800

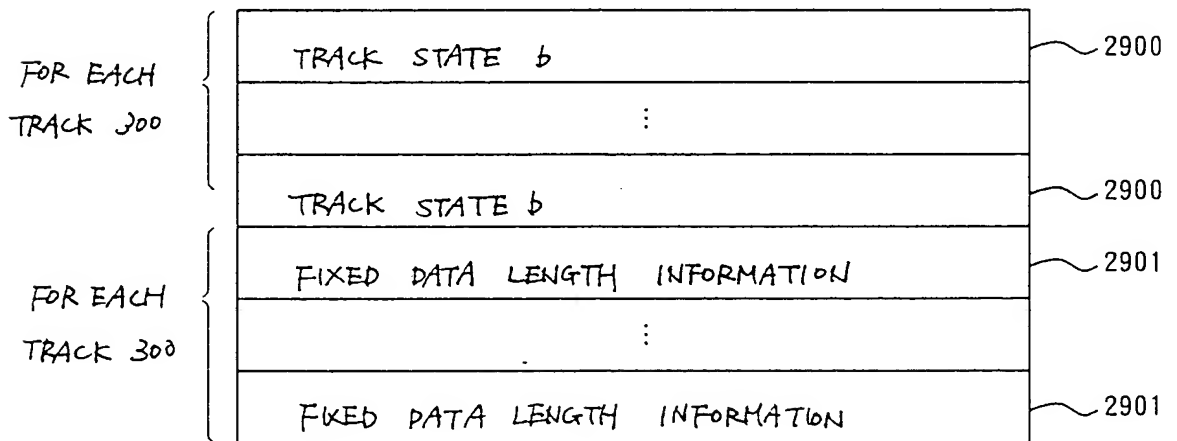


FIG. 38

2900

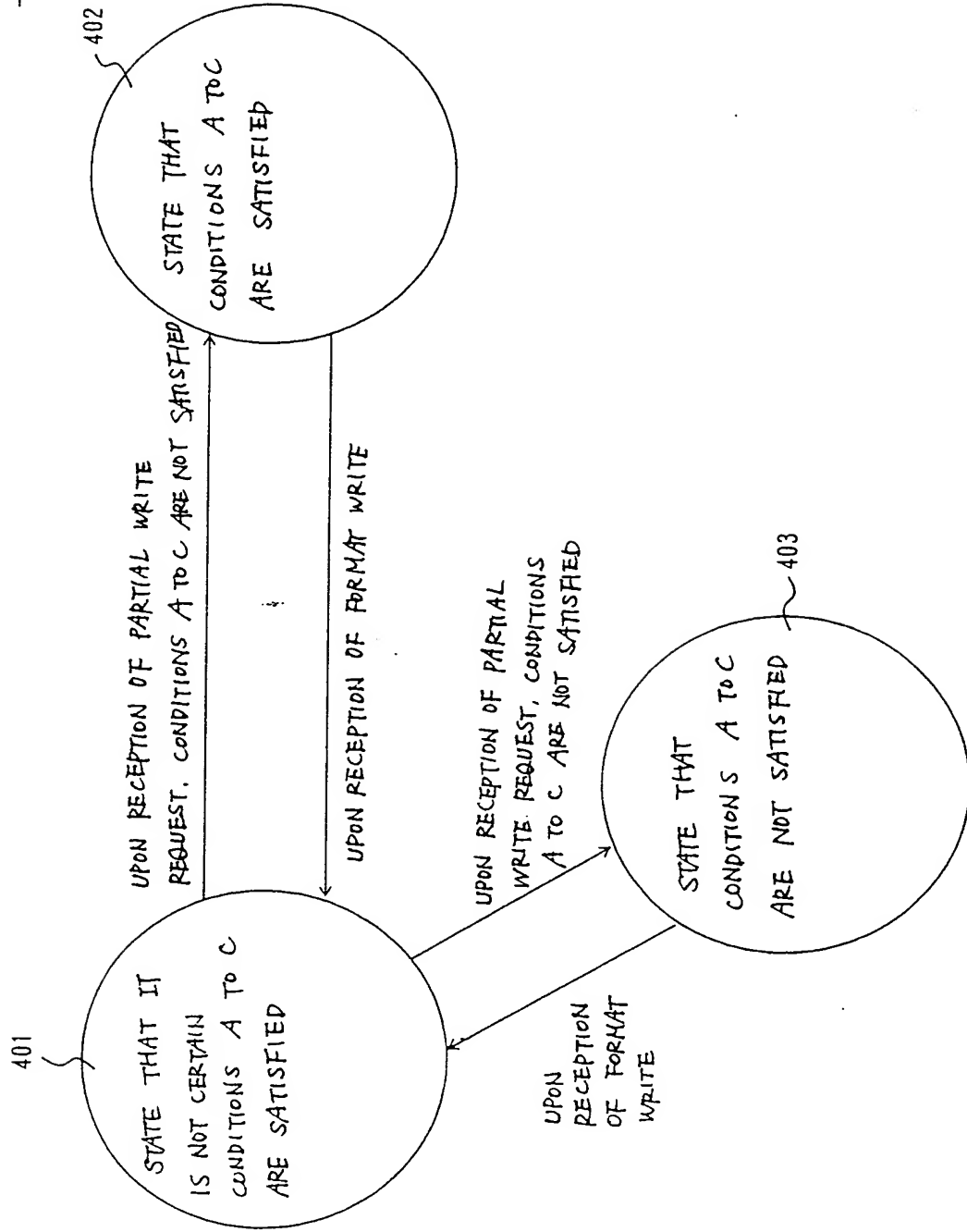


FIG. 39

10

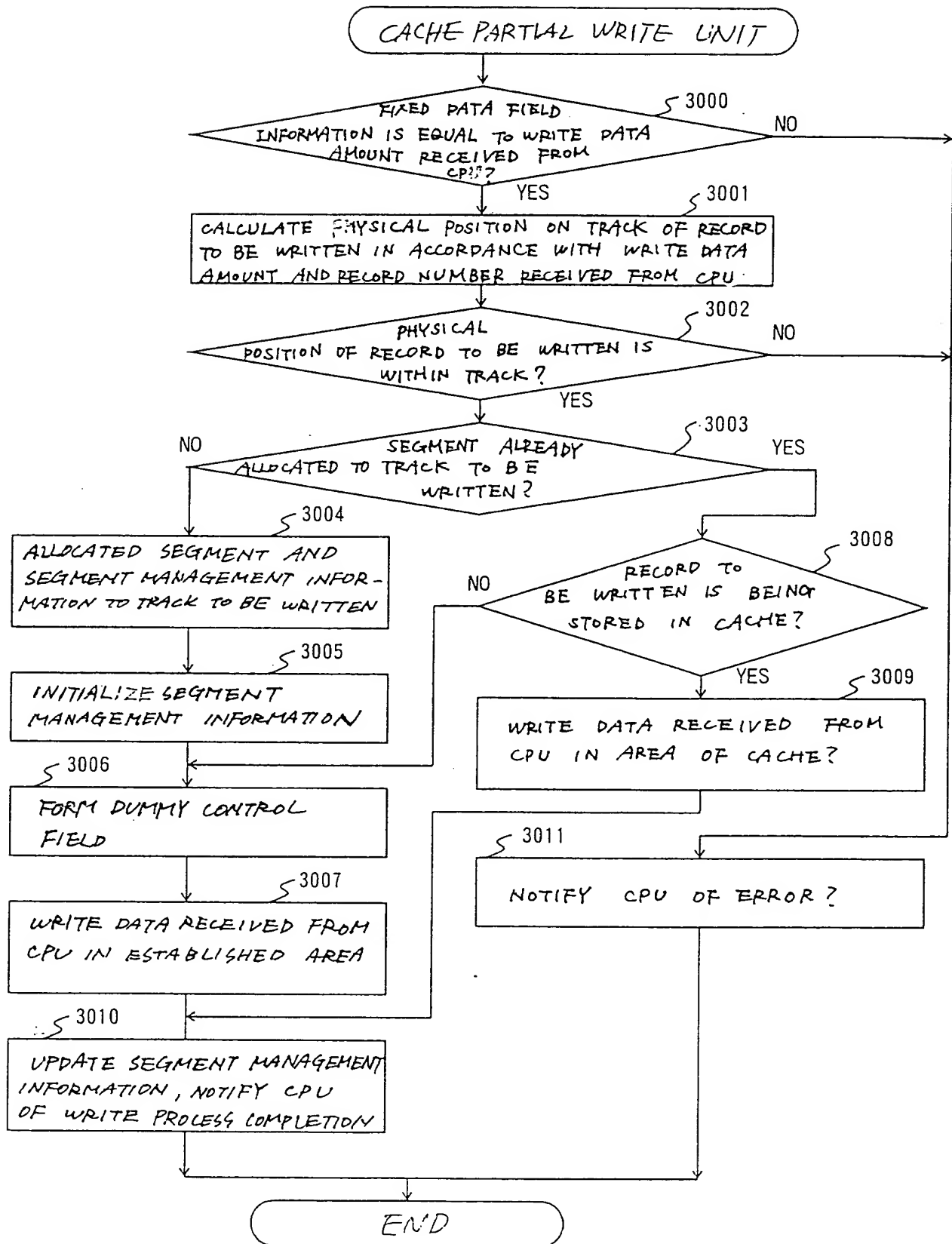


FIG. 40 A

14

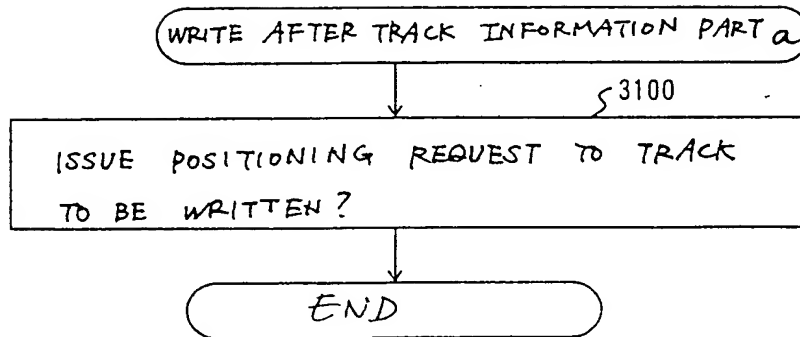


FIG. 40 B

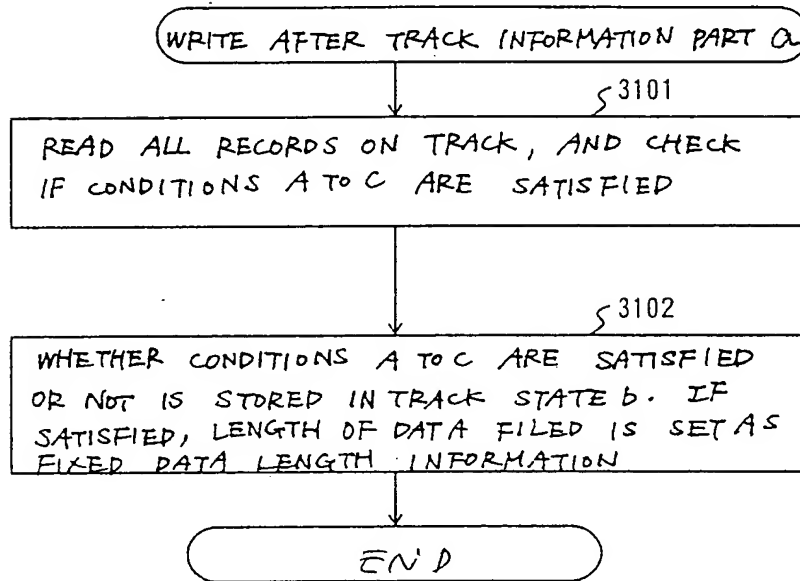


FIG. 41

15

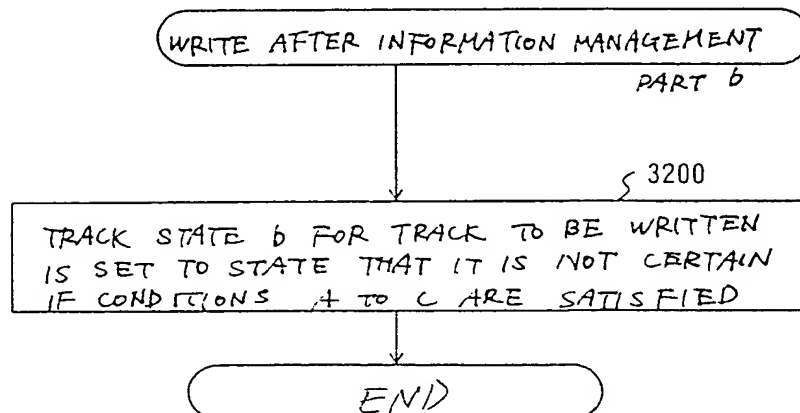


FIG. 42

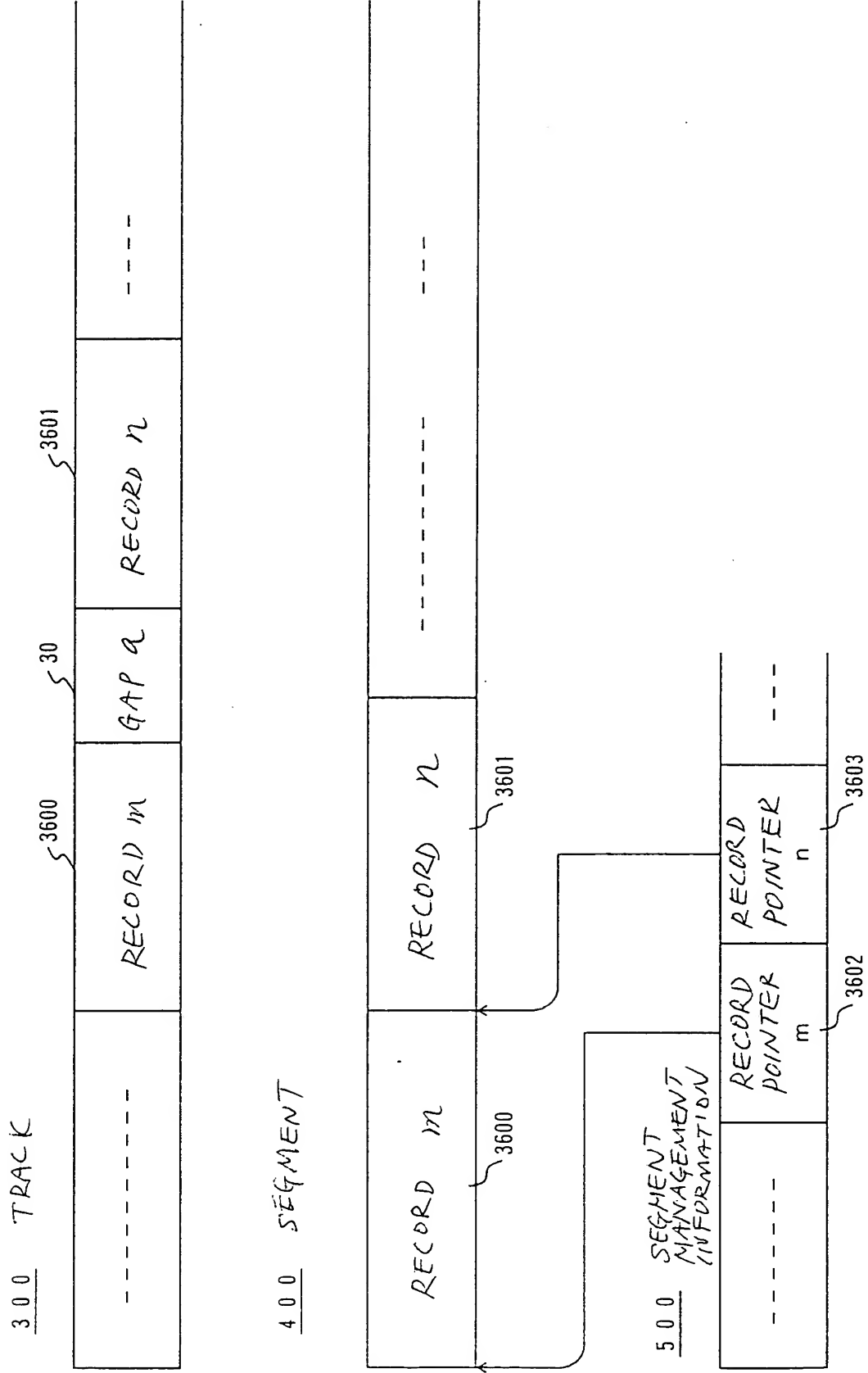


FIG. 43 A

4 0 0 SEGMENT  
§ 4300

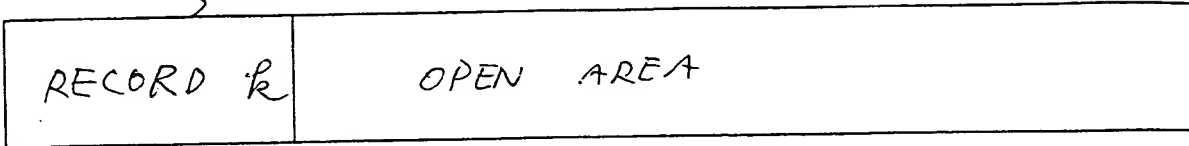


FIG. 43 B

4 0 0 SEGMENT  
§ 4301

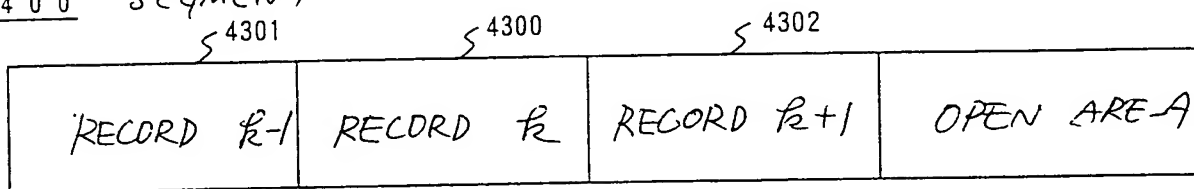
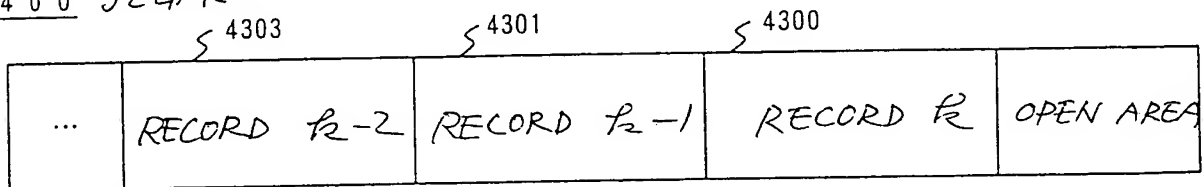


FIG. 43 C

4 0 0 SEGMENT  
§ 4303





# FIG. 44

500

AS MANY AS THE  
NUMBER OF RECORD  
NUMBERS 602  
DEFINABLE WITHIN  
TRACK 300

	WRITE AFTER EXECUTING FLAG	800
	PARTIAL WRITE FLAG	801
	CACHED TRACK NUMBER	805
{	RECORD POINTER	806
	:	
{	RECORD POINTER	806
	UPDATE RECORD POINTER	807
{	:	
	UPDATE RECORD POINTER	807
{	UPDATE FIELD INFORMATION	808
	:	
{	UPDATE FIELD INFORMATION	808
	INTRA-SEGMENT EMPTY AREA ADDRESS	809
	SEGMENT POINTER	810
	WRITE POSITION CALCULATION POSSIBLE BIT	2200
	FIXED DATA FIELD LENGTH	2201
	INTRA-CACHE MAXIMUM RECORD NUMBER	3700
	:	

FIG. 45

